

AT32F4xx Timer Input Capture Mode

Introduction

This sample code demonstrates how to use timer input capture function in AT32F4xx series.

Applicable products:

Product series	AT32Fxx series
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List of major peripherals used:

Peripherals	TIMER
	USART

1 Quick start

1.1 Hardware resources

AT-START-F403A V1.0 evaluation board

1.2 Software resources

1. This Demos is based on AT32F403A, and AT32F403A_407_Firmware_Library_V2.x.x (BSP version)
2. TMR3_CH2 is used as an input capture channel for capturing frequency of external signals. The minimum frequency is 3.66 KHz (240MHz/65536) for this Demo able to collect external signals.

1.3 Example case

- 1) Connect the to-be-captured external signals to TMR3_CH2 (PA7)
- 2) Print signal frequency via USART1_TX at every 1 second. This print interval can be adjusted by changing latency in the code.

Table 1. Document revision history

Date	Revision	Changes
2021.12.03	2.0.0	Initial release

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